



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Zahi Said Abuhamdeh et al.

SERIAL NO.: 10/647,018

GROUP ART UNIT:

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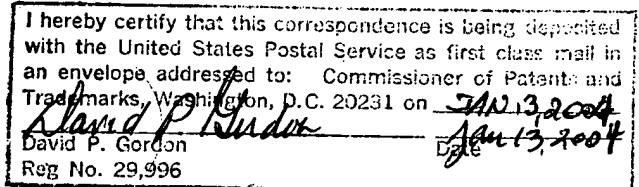
EXAMINER:

FOR: Microprocessor Based
Self-Diagnostic Port

ATT'Y DOCKET: TRA-078

Honorable Commissioner of Patents
and Trademarks
Washington, D.C. 20231

Sir:



PRELIMINARY AMENDMENT

Kindly amend the application as follows.

In the Specification:

Page 1, line 2, insert the following paragraph.

This application claims the benefit of provisional application Serial Number 60/445,407, filed February 6, 2003, entitled "Novel JTAG Master for Support of Chip Level Self-Diagnostics", the complete disclosure of which is hereby incorporated by reference herein.